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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: LEIJTEN, Jeroen
Application No.: 10/059427 Conf.:
Date Filed: 01/29/2002Docket No.: NL 010073
Art Unit: 2183
Examiner: Rizzuto, Kevin

Title: COMPUTER INSTRUCTION WITH INSTRUCTION FETCH CONTROL BITS

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450TRANSMITTAL LETTER

Sir:

There are a total of 2 pages in this submission which includes this transmittal letter, as well as the documents listed below:


☒ Response to Restriction Requirement - 1 page☐☐

The Commissioner is hereby requested and authorized pursuant to 37 CFR §1.136(a)(3), to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. Please charge any additional fees which may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

Date: 11/2/04

Respectfully submitted,

By


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CERTIFICATE OF MAILING or TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450," or facsimile transmitted to the USPTO at (703) 872-9306, on the date indicated below.

(Date)

11/2/2004

(Signature)

(Name)

Daniel L. Michalek

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